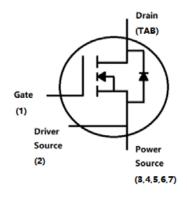




S2M0040120J-1 1200V SIC POWER MOSFET



Circuit Diagram



Description

S2M0040120J-1 is single SiC Power MOSFET packaged in TO-263-7 case. The device is a high voltage n-channel enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0040120J-1 is ideal for energy sensitive, high frequency applications in challenging environments.

Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) = $45m\Omega$.
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin

Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	V _{DSS}	V _{GS} = 0V, I _{DS} = 100uA, T _j = 25°C	1200	V
Gate Source Voltage	V _{GSS}	T _j = 25°C, Absolute maximum values, AC (f>1Hz)	-10 to 25	V
Gate Source Voltage	V _{GSOP}	T _j = 25°C Recommended Operational Values	-5 to 20	V
Continuous Drain Current	I _D	V _{GS} = 20V, T _j = 25°C	55	Α
	I _D	V _{GS} = 20V, T _j = 100°C	39	А
Pulsed Drain Current	I _{D,pulse}	Pulse width t _P limited by T _{jmax}	160	А
Power Dissipation	P _D	T _C =25°C, T _J = 175 °C	348	W
Solder Temperature	TL	1.6mm (0.063") from case for 10s	260	°C

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Electrical Characteristics(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Units	
Drain Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 100uA	1200			V	
Gate Threshold Voltage	.,	$V_{DS} = V_{GS}$, $I_D = 10$ mA	1.8	2.4	4	V	
	$V_{\text{GS(th)}}$	V _{DS} = V _{GS} , I _D = 10mA, T _J = 175 °C		1.55		V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 1200V, V _{GS} = 0V		1	100	uA	
Gate Source Leakage Current	I _{GSS}	V _{GS} = 20V, V _{DS} = 0V			250	nA	
Desire Courses On Otata Desirtan	-	V _{GS} = 20V, I _D = 40A		45	52	mΩ	
Drain Source On-State Resistance	$R_{DS(on)}$	V _{GS} = 20V, I _D = 40A, T _J = 175 °C		73		mΩ	
Tourse		V _{DS} = 20 V, I _{DS} = 40 A		10		S	
Transconductance	gfs	V _{DS} = 20 V, I _{DS} = 40 A, T _J = 175 °C		12		S	
Input Capacitance	C _{ISS}	V _{GS} = 0V,		1904			
Output Capacitance	Coss	V _{DS} = 1000V		108		pF	
Reverse Transfer Capacitance	C _{RSS}	V _{AC} = 25mV f = 1MHz		6			
Coss Stored Energy	Eoss	1 - 1101112		72.9		uJ	
Turn-On Switching Energy	Eon	V _{DS} = 800V, V _{GS} = -5/20V		0.25			
Turn-Off Switching Energy	E _{OFF}	I _D =40A, R _{G(ext)} =2.5Ω, L=99uH		0.05		mJ	
Turn-On Delay Time	t _{d(on)}	V _{DS} = 800V, V _{GS} = -5/20V		12			
Rise Time	t _r	$I_D = 40A, R_{G(ext)} = 2.5\Omega$		14			
Turn-Off Delay Time	$t_{d(off)}$	Inductive Load Timing relative to VDS Per IEC60747-8-4 pg 83		22		ns	
Fall Time	t _f			4			
Internal Gate Resistance	R _{G(int)}	f = 1MHz, VAC = 25 mV		2.6		Ω	
Gate to Source Charge	Qgs	V _{DS} = 800V, V _{GS} = -5/20V		34.3			
Gate to Drain Charge	Q_{gd}	I _D = 40A		32.1		nC	
Total Gate Charge	Qg	Per IEC60747-8-4 pg 21		92.1			

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Reverse Diode Characteristics:

Characteristics	Symbol Condition		Тур.	Max.	Units
	$V_{ ext{SD}}$	V _{GS} = -5V, I _{SD} = 20A	3.6		V
Diode Forward Voltage		V _{GS} = -5V, I _{SD} = 20A, T _J =175°C	3.2		V
Continuous Diode Forward Current	ls	T _C =25°C	44		
Reverse Recovery Time	t _{rr}	V _{GS} =-5V, I _{SD} =40A, T _J =25°C	43.4		ns
Reverse Recovery Charge	Q_{rr}	V _R =800V	162		nC
Peak Reverse Recovery Current	I _{mm}	dif/dt=1047A/µs	8.1		Α

Thermal-Mechanical Specifications:

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T _{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	$R_{ heta JC}$	DC operation	0.44	°C/W
Typical Thermal Resistance Junction to Ambient	R _{0JA}		32.6	°C/W
Approximate Weight	wt	-	1.44	g

Ordering Information:

Device	Package	Shipping
S2M0040120J-1TR	TO-263-7	800pcs/reel
S2M0040120J-1	TO-263-7	50pcs/tube

Marking Diagram



Where XXXXX is YYWWL

S2M = Device Type

 $0040 = R_{DS}(on)$

120 = Reverse Voltage (1200V)

J = Package SSG = SSG YY = Year WW = Week L = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0

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Ratings and Characteristics Curves

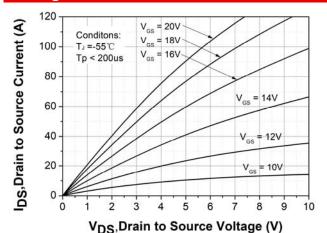


Figure 1. Output Characteristics T_J = -55 °C

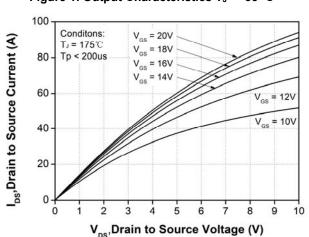


Figure 3. Output Characteristics T_J = 175°C

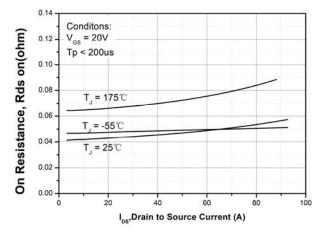


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

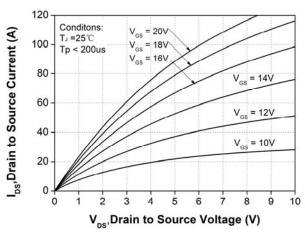


Figure 2. Output Characteristics T_J = 25 °C

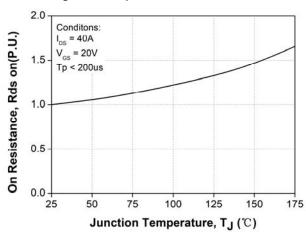


Figure 4. Normalized On-Resistance vs. Temperature

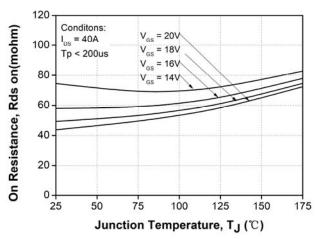


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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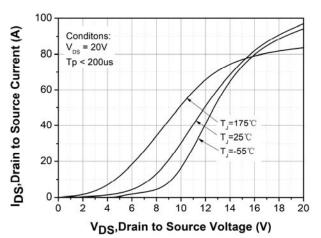


Figure 7. Transfer Characteristic for Various Junction Temperatures

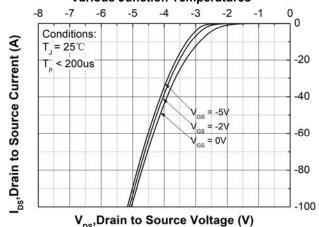


Figure 9. Body Diode Characteristic at T_J = 25 °C

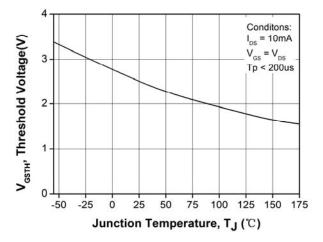


Figure 11. Threshold Voltage vs. Temperature

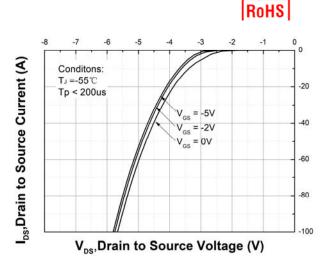


Figure 8. Body Diode Characteristic at T_J = -55 °C

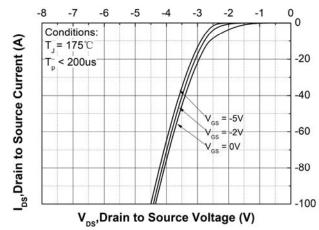


Figure 10. Body Diode Characteristic at T_J = 175 °C

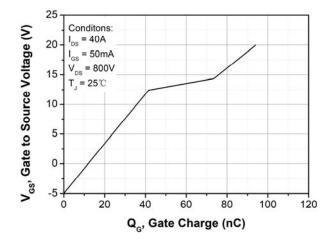


Figure 12. Gate Charge Characteristic

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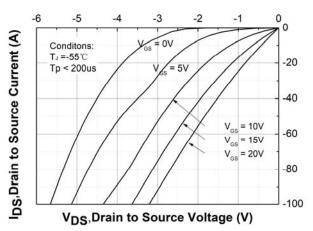


Figure 13. 3rd Quadrant Characteristic at T_J = -55 °C

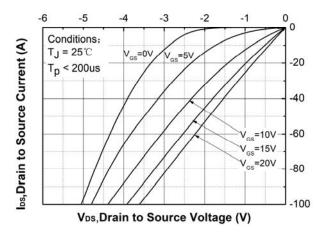


Figure 15. 3rd Quadrant Characteristic at T_J = 175°C

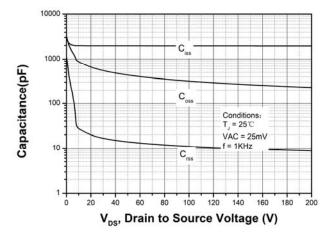


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)



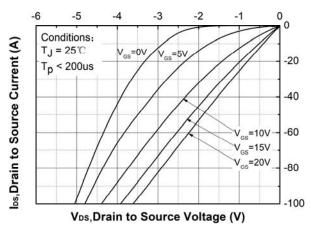


Figure 14. 3rd Quadrant Characteristic at T_J = 25 °C

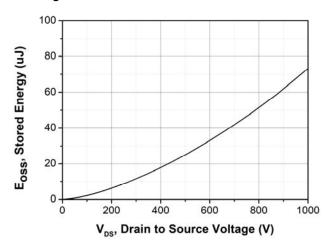


Figure 16. Output Capacitor Stored Energy

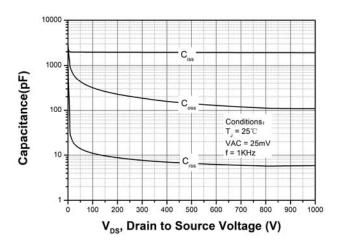


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

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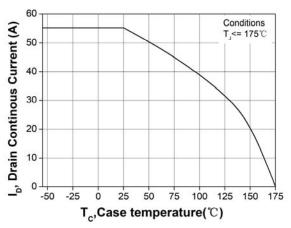


Figure 19. Continuous Drain Current Derating vs.
Case Temperature

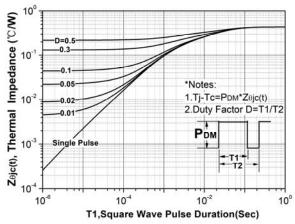


Figure 21. Transient Thermal Impedance (Junction - Case)

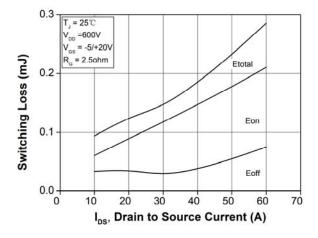


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V_{DD} = 600V)



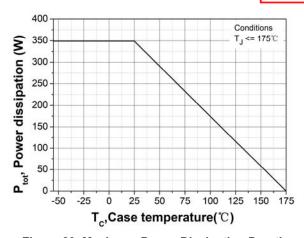


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

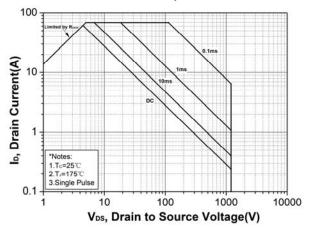


Figure 22. Safe Operating Area

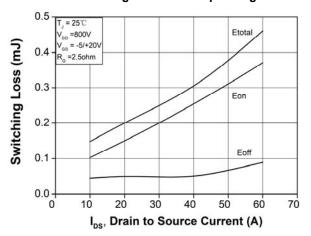


Figure 24. Clamped Inductive Switching Energy vs. Drain Current (V_{DD} = 800V)

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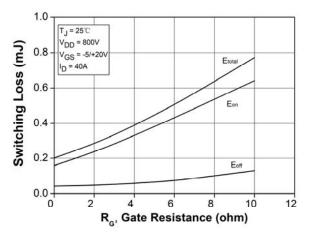


Figure 25. Clamped Inductive Switching Energy vs. R_{G(ext)}

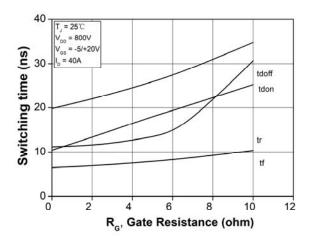


Figure 27. Switching Times vs. R_{G(ext)}

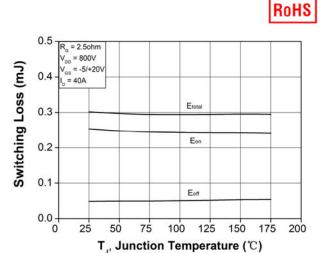


Figure 26. Clamped Inductive Switching Energy vs.
Temperature

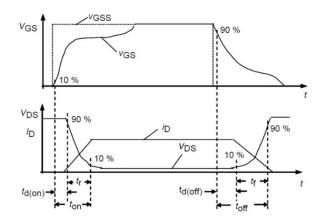
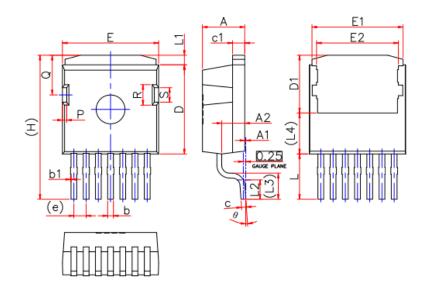


Figure 28. Switching Times Definition





Mechanical Dimensions TO-263-7



SYMBOL	Millimeters				
STWIBUL	MIN.	TYP.	MAX.		
Α	4.300		4.400		
A1	0.000		0.100		
A2	2.300		2.400		
b	0.500		0.600		
b1	0.000		0.075		
С	0.400		0.500		
c1	1.170		1.270		
D	9.050		9.250		
D1	5.900		6.000		
E	9.800	10.0			
E1	9.360		9.460		
E2	8.400		8.500		
е		1.270 REF			
Н		15.000 REF			
L	4.200	2.512	4.700		
L1	0.700	0.60	1.000		
L2	1.700 1.418		2.000		
L3	2.700 REF				
L4	4.250 REF				
Р	0.350		0.450		
Q	4.020		4.120		
R	2.030 2.13		2.130		
S	1.400 1.500				
θ	0°		4°		

S2M0040120J-1



Technical Data Data Sheet N2699, REV.A



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